## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of:

Brian S. Beaman

Atty. Docket:YOR919990088US1

Brian S. Beaman et al.

Art Unit: 2829

Serial No.: 09/251,988

Examiner: Jermele M. Hollington

Filed: February 17, 1999

For: Title

STRUCTURAL DESIGN AND PROCESSES TO CONTROL PROBE

POSITION ACCURACY IN A WAFER TEST PROBE ASSEMBLY

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Attached is an Information Disclosure Statement and Notice of Citation of references of relating copending U.S. Application No.: 10/928,473, filed August 27, 2004.

The listing of these materials is not to be construed as an admission that such is actually prior art with respect to the invention of the present application and is not to be construed that such is material with respect to the present invention.

Since these documents are being submitted with an Request for Continued Examination request, applicants believe no fee is due.

Please charge any fee necessary to enter this paper and any previous paper to deposit account 09-0468.

Respectfully submitted

Daniel B. Morris, Esq Registration No.: 32,053

**IBM** Corporation

P.O. Box 218

Yorktown Heights, NY 10598 Telephone: 914-945-3217

Date: April 24, 2006

Serial Number: 09/251,988

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